

N-Channel SiC Power MOSFET

V_{DS}	=	1700 V
$R_{DS(on)}$	=	1.0Ω
$I_D@25^{\circ}C$	=	5 A

Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitance
- Easy to Parallel and Simple to Drive

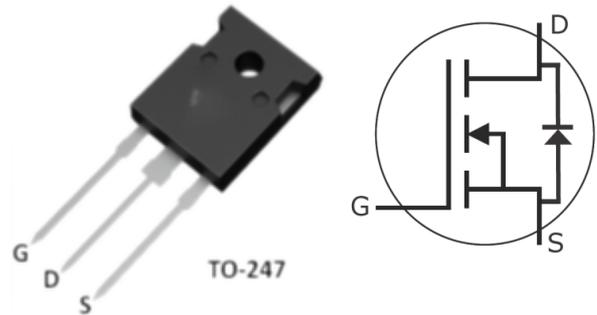
Benefits

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

Applications

- Auxiliary Power Supplies
- Switch Mode Power Supplies

Package



Part Number	Package
NF3M10017D	TO-247-3

Maximum Ratings ($T_c=25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{DSmax}	Drain-Source Voltage	1700	V	$V_{GS}=0V, I_D=100\mu A$	
V_{GSmax}	Gate-Source Voltage	-10/+25	V	Absolute maximum values	
V_{GSop}	Gate-Source Voltage	-5/+20	V	Recommended operational values	
I_D	Continuous Drain Current	5.0	A	$V_{GS}=20V, T_c=25^{\circ}C$	
		3.5		$V_{GS}=20V, T_c=100^{\circ}C$	
$I_{D(pulse)}$	Pulsed Drain Current	6.0	A	Pulse width t_p limited by T_{Jmax}	
P_D	Power Dissipation	69	W	$T_c=25^{\circ}C, T_J=150^{\circ}C$	
T_J, T_{STG}	Operating Junction and Storage Temperature	-55 to +150	$^{\circ}C$		

Electrical Characteristics (T_c=25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V _{(BR)DSS}	Drain-Source Breakdown Voltage	1700	/	/	V	V _{GS} =0V, I _D =100μA	
V _{GS(th)}	Gate Threshold Voltage	2.5	3.0	4.5	V	V _{DS} =V _{GS} , I _D =1mA	
		/	2.2	/		V _{DS} =V _{GS} , I _D =1mA, T _J =150°C	
I _{DSS}	Zero Gate Voltage Drain Current	/	1	100	μA	V _{DS} =1700V, V _{GS} =0V	
I _{GSS+}	Gate-Source Leakage Current	/	/	250	nA	V _{DS} =0V, V _{GS} =25V	
I _{GSS-}	Gate-Source Leakage Current	/	/	250	nA	V _{DS} =0V, V _{GS} =-10V	
R _{DS(on)}	Drain-Source On-State Resistance	/	1.0	1.3	Ω	V _{GS} =20V, I _D =2A	
		/	1.5	/		V _{GS} =20V, I _D =2A, T _J =150°C	
g _{fs}	Transconductance	/	1.15	/	S	V _{DS} =20V, I _D =2 A	
		/	1.30	/		V _{DS} =20V, I _D =2A, T _J =150°C	
C _{iss}	Input Capacitance	/	186	/	pF	V _{GS} =0V	
C _{oss}	Output Capacitance	/	12	/		V _{DS} =1000V	
C _{rss}	Reverse Transfer Capacitance	/	1.6	/		f=1MHz	
E _{OSS}	C _{oss} Stored Energy	/	6.2	/	μJ	V _{AC} =25mV	
E _{ON}	Turn-On Switching Energy	/	48	/		V _{DS} =1200V, V _{GS} =-5V/20V	
E _{OFF}	Turn-Off Switching Energy	/	18	/		I _D =2A, R _{G(ext)} =2.5Ω, L=1500μH	
t _{d(on)}	Turn-On Delay Time	/	5.2	/	ns	V _{DS} =1200V, V _{GS} =-5V/20V, I _D =2A R _{G(ext)} =2.5Ω, R _L =600Ω	
t _r	Rise Time	/	9.4	/			
t _{d(off)}	Turn-Off Delay Time	/	13.2	/			
t _f	Fall Time	/	22.0	/			
R _G	Internal Gate Resistance	/	22	/	Ω	f=1MHz open drain	
Q _{GS}	Gate to Source Charge	/	5.2	/	nC	V _{DS} =1200V	
Q _{GD}	Gate to Drain Charge	/	7.3	/		V _{GS} =-5V/20V	
Q _G	Total Gate Charge	/	21.8	/		I _D =2A	

Reverse Diode Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V _{SD}	Diode Forward Voltage	4.2	/	V	V _{GS} =-5V, I _{SD} =1A	
		3.9	/		V _{GS} =-5V, I _{SD} =1A, T _J =150°C	
I _S	Continuous Diode Forward Current	/	4	A	T _c =25°C	
t _{rr}	Reverse Recover Time	25	/	ns	V _{GS} =-5V, V _R =1200V, I _{SD} =2A	
Q _{rr}	Reverse Recovery Charge	15	/	nC		
I _{rrm}	Peak Reverse Recovery Current	2.8	/	A		

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
R _{θJC}	Thermal Resistance from Junction to Case	1.8	2.0	°C/W		

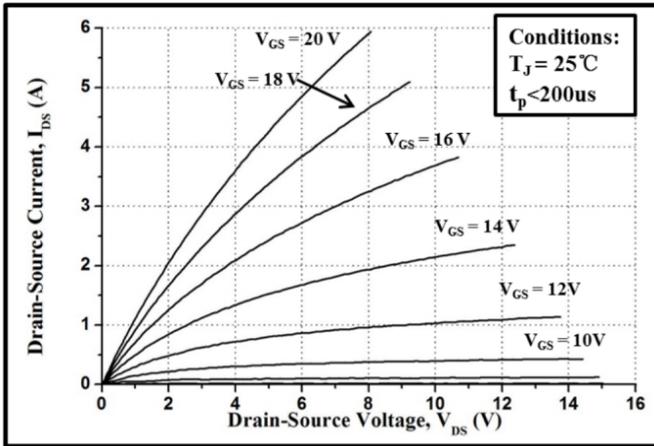


Figure 1. Typical Output Characteristics $T_J = 25^\circ\text{C}$

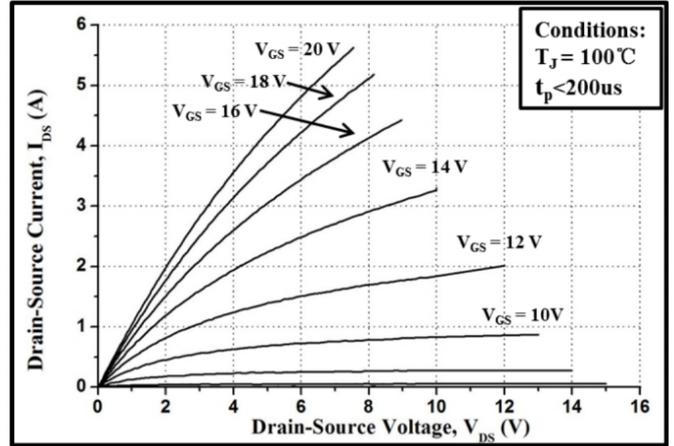


Figure 2. Typical Output Characteristics $T_J = 100^\circ\text{C}$

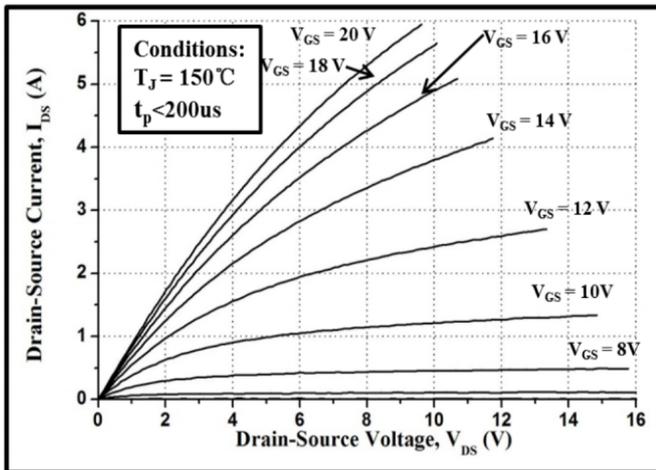


Figure 3. Typical Output Characteristics $T_J = 150^\circ\text{C}$

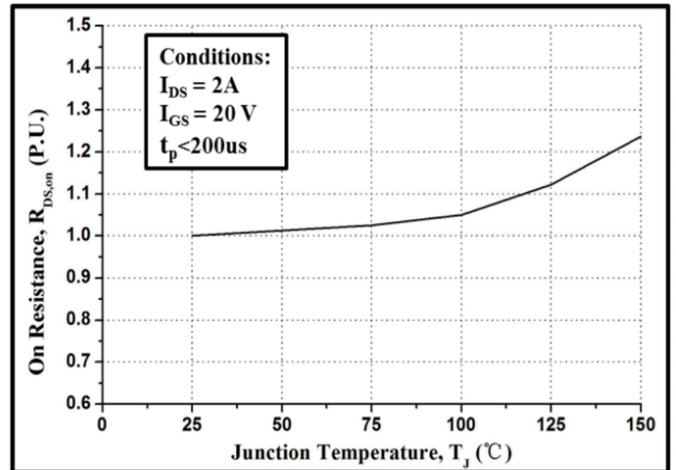


Figure 4. Normalized On-Resistance vs. Temperature

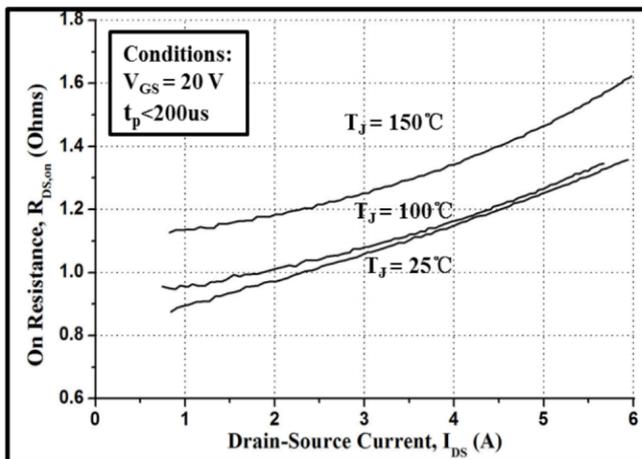


Figure 5. On-Resistance vs. Drain Current

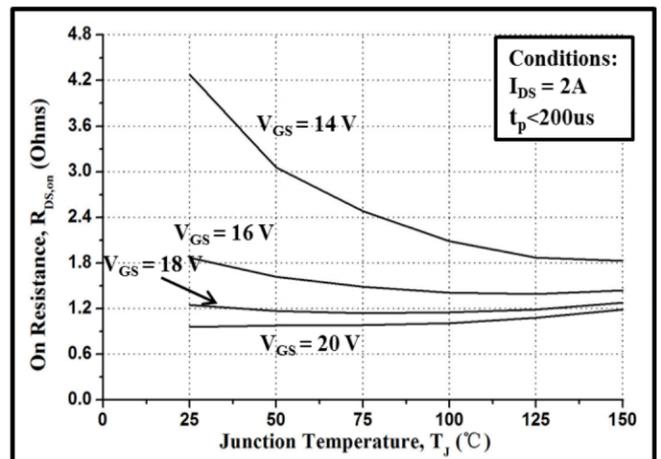


Figure 6. On-Resistance vs. Temperature

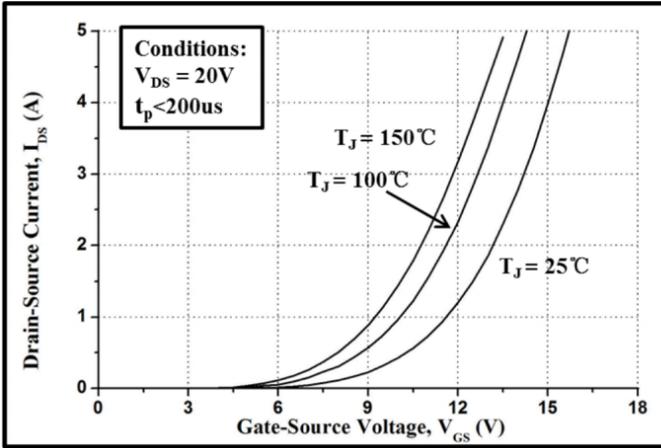


Figure 7. Typical Transfer Characteristics

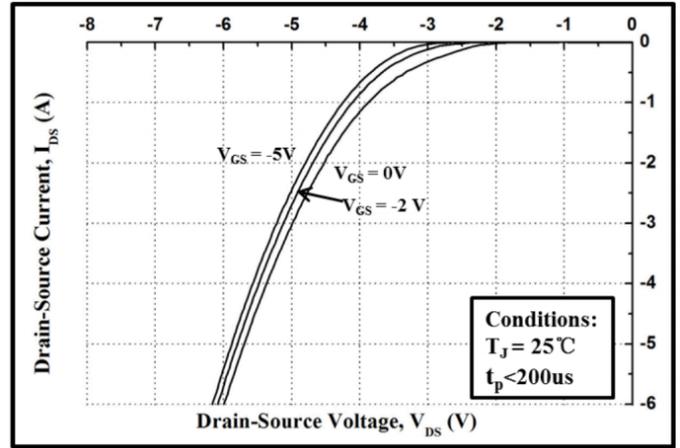


Figure 8. Body Diode Characteristics at 25°C

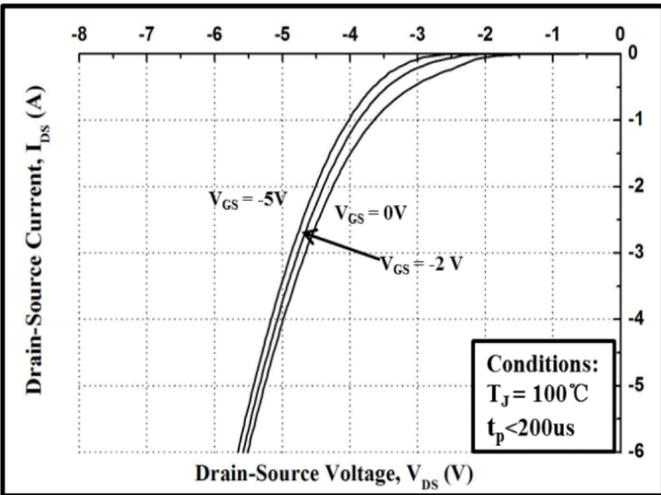


Figure 9. Body Diode Characteristics at 100°C

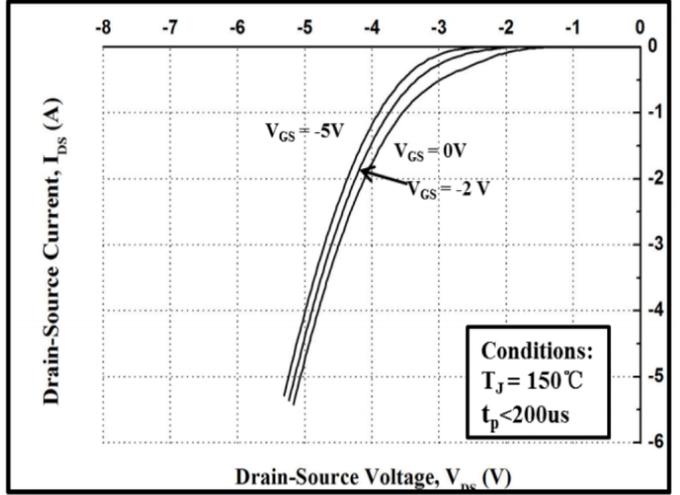


Figure 10. Body Diode Characteristics at 150°C

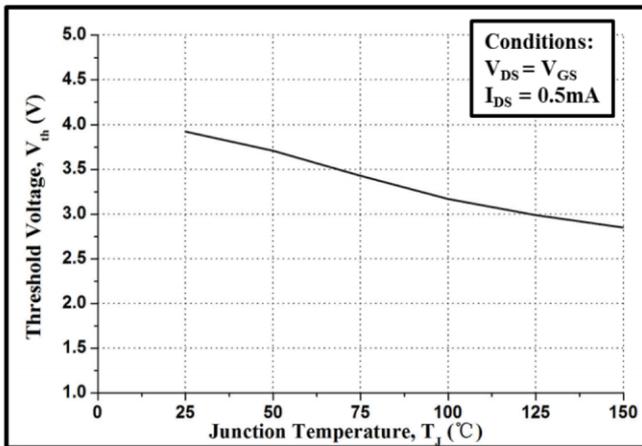


Figure 11. Gate Threshold Voltage vs. Temperature

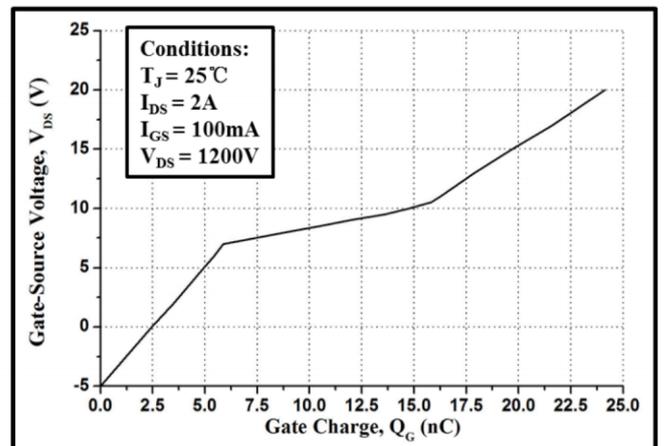


Figure 12. Gate Charge Characteristic

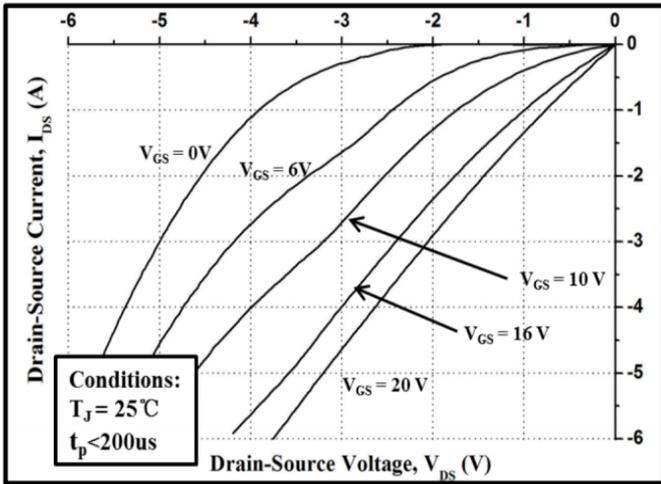


Figure 13. 3rd Quadrant Characteristics at 25°C

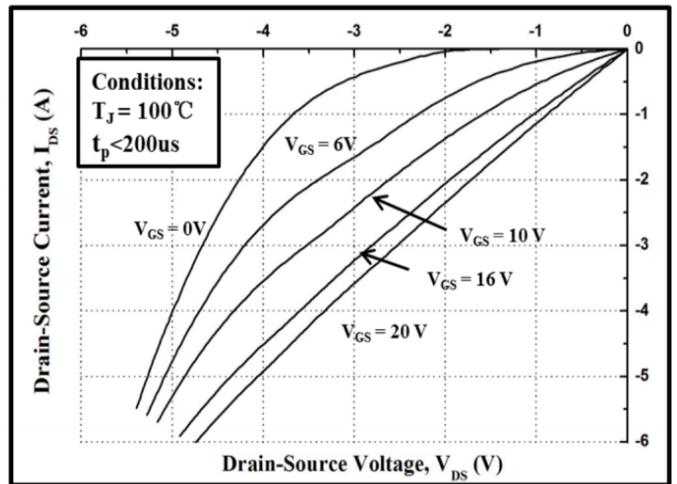


Figure 14. 3rd Quadrant Characteristics at 100°C

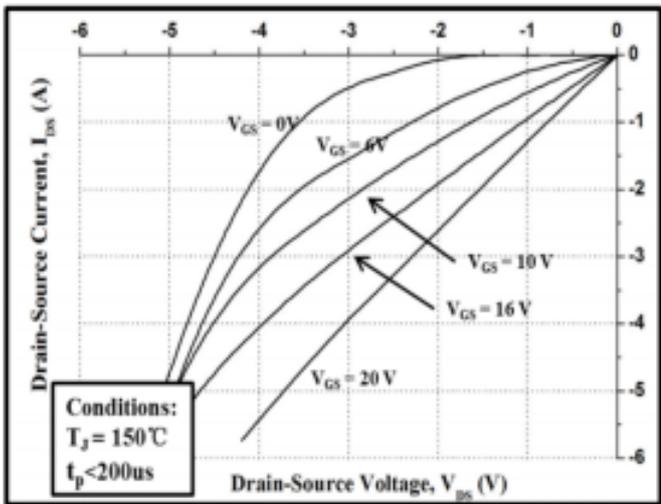


Figure 15. 3rd Quadrant Characteristics at 150°C

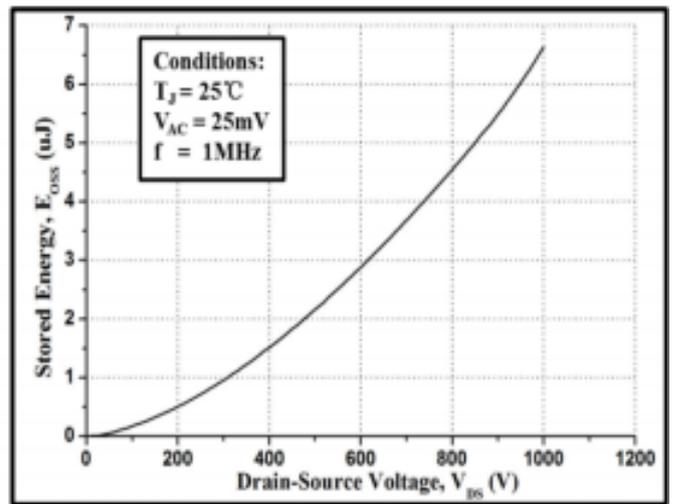


Figure 16. Output Capacitor Stored Energy

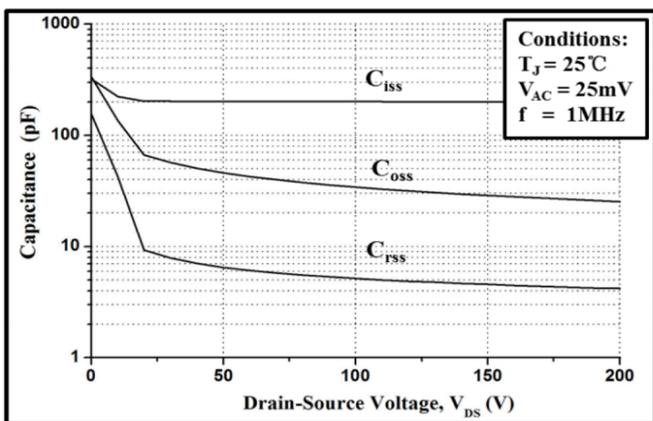


Figure 17. Capacitances vs. Drain-Source Voltage

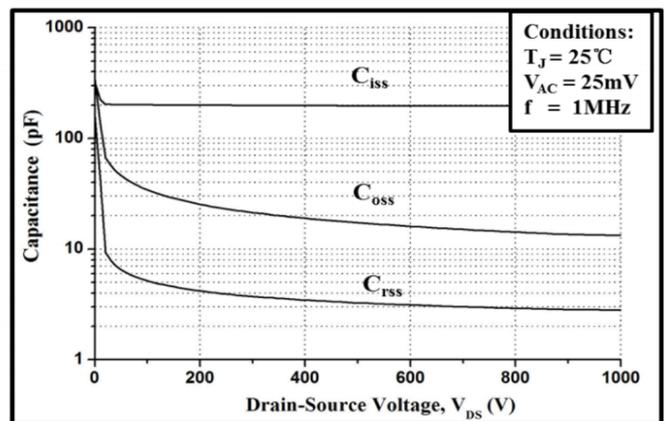


Figure 18. Capacitances vs. Drain-Source Voltage

